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Given an electronic system modeled at the electronic system level (ESL), EDA automates the design and test processes of verifying the correctness of the ESL design against the specifications of the electronic system, taking the ESL design through various synthesis and verification steps, and finally testing the manufactured electronic system to ensure that it meets the specifications and quality requirements of the electronic system.

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Electronic Design Automation: Synthesis, Verification, and ...

Electronic Design Automation: Synthesis, Verification, and Test Laung-Terng Wang , Yao-Wen Chang , Kwang-Ting (Tim) Cheng Morgan Kaufmann , Mar 11, 2009 - Technology & Engineering - 972 pages

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Electronic design automation (EDA), also referred to as electronic computer-aided design (ECAD), is a category of software tools for designing electronic systems such as integrated circuits and printed circuit boards.The tools work together in a design flow that chip designers use to design and analyze entire semiconductor chips. Since a modern semiconductor chip can have billions of ...

Electronic design automation - Wikipedia

Switching circuits and logic design, or by instructor's consent Main lecture basis Lecture slides and/or handouts Textbook Y.-W. Chang, K.-T. Cheng, and L.-T. Wang (Editors). Electronic Design Automation: Synthesis, Verification, and Test. Elsevier, 2009. Reference S. H. Gerez. Algorithms for VLSI Design Automation. John

Introduction to Electronic Design Automation

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Electronic Design Automation (EDA) - Synopsys

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Field-programmable gate array - Wikipedia

Electronic Design Automation For Integrated Circuits Handbook, by Lavagno, Martin, and Scheffer, ISBN 0-8493-3096-3 A survey of the field of Electronic design automation. The above summary was derived, with permission, from Volume 2, Chapter 2, Logic Synthesis by Sunil Khatri and Narendra Shenoy.

Logic synthesis - Wikipedia

Not only does it play a crucial role in the electronic design automation flow, its techniques also find broader applications in formal verification, software synthesis, and other fields. This chapter covers classic elements of logic synthesis for combinational circuits.

Electronic Design Automation | ScienceDirect

Key words: Electronic Design Automation, Logic simulation performance, large scale software design and implementation, C/C++/Perl, parallel/multi-threaded software development, technical ...

Vidyasagar Nookala - Sr Staff R&D Engineer, Verification ...

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